

IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

First Named Inventor: Thomas J Heller JR : GROUP ART UNIT 2187

Pat Appln: Serial No. 10/603251: Examiner: Kalpit Parikh

Filed: June 25, 2003 : Date: September 6, 2007

Title: Multiprocessor System with Dynamic Cache Coherency Regions :

Attorney Docket: POU920020123US1 :

Deposit Account: 09-0463 :

Mailing Address: Lynn L. Augspurger, Attorney, Reg No. 24,227 :  
IBM Intellectual Property Law  
2455 South Road, P386  
Poughkeepsie, NY 12601

CORRECTED AMENDMENT (Responsive to OA Dated 3/22/2007)

EXPEDITED PROCESSING REQUESTED

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

A. Introductory Comments:

In reply to the Official action mailed 3/22/2007 and the Notice of Non-Compliant Amendment dated 8/30/2007 Applicants respectfully request entry of the following corrected amendment to place the application in condition for allowance in light of the remarks and these amendments. (The Commissioner is also hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 09-0463. Those expected are already part of the separate fee authorization.)